

VIDEO SIGNAL PROCESSOR AND TV RECEIVER

USING THE SAME

ABSTRACT OF THE DISCLOSURE

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A video signal processor for improving a detection precision of Y motion and C motion, preventing erroneous judgment, and preventing deterioration of image quality without being influenced by the band of the luminance
10 signal or the phase of a sub-carrier of chroma. The processor includes a provisional 3D Y/C separation motion detection circuit, extracting Y signals of a current frame and a past frame based on composite video signals of three adjacent lines of the current frame and the past
15 frame, detecting Y motion in accordance with a difference of Y signals of the current frame and the past frame by a Y motion detection unit, outputting a Y motion detection signal MVD_y , detecting C motion by a C motion detection unit based on Y signals of three adjacent lines in the
20 current frame, outputting a C motion detection signal MVD_c , and selecting, according to the motion detection signals MVD_y and MVD_c , the difference of the C signals separated from the current frame and the past frame or a

predetermined value 0 to generate a C motion coefficient.